

FIG. 1 (PRIOR ART)

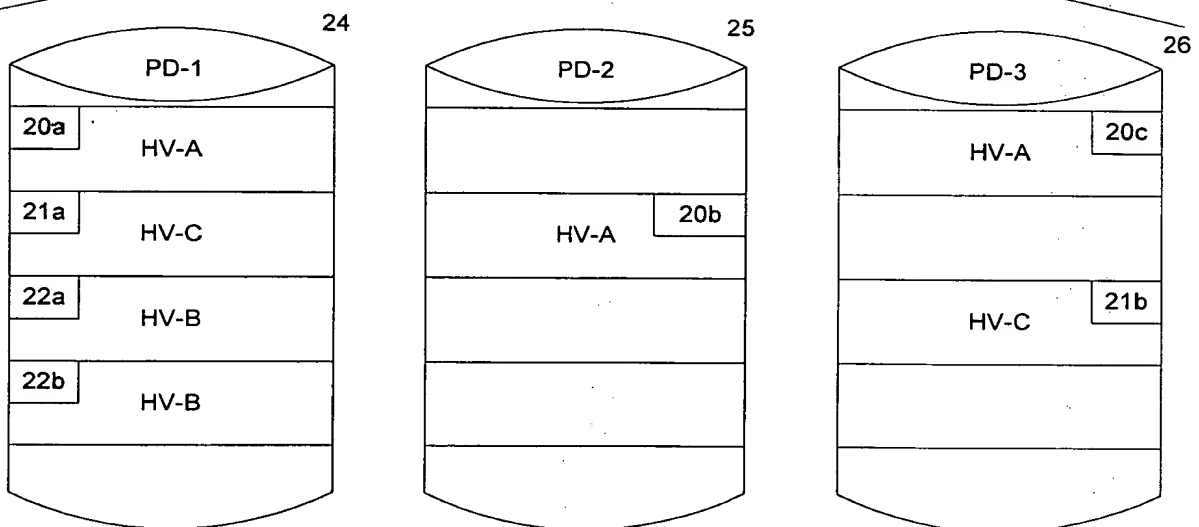
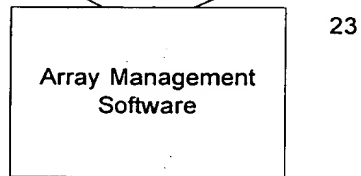
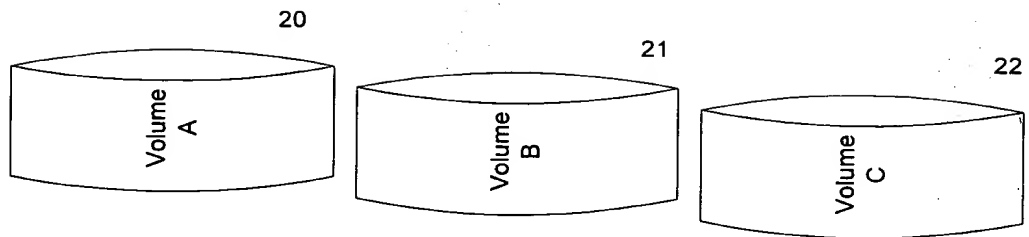


FIG. 2A (PRIOR ART)

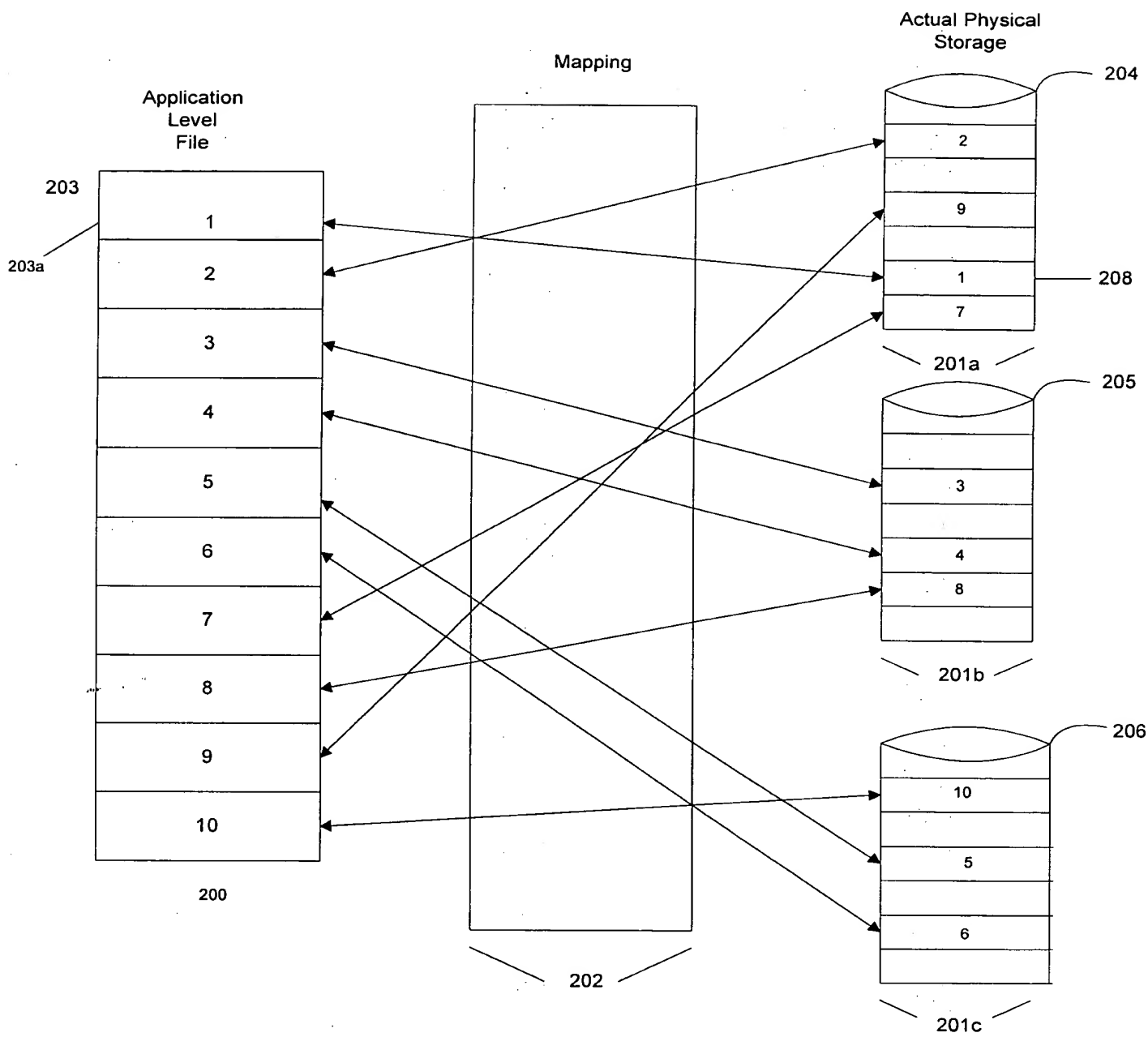


FIG. 2B

LO

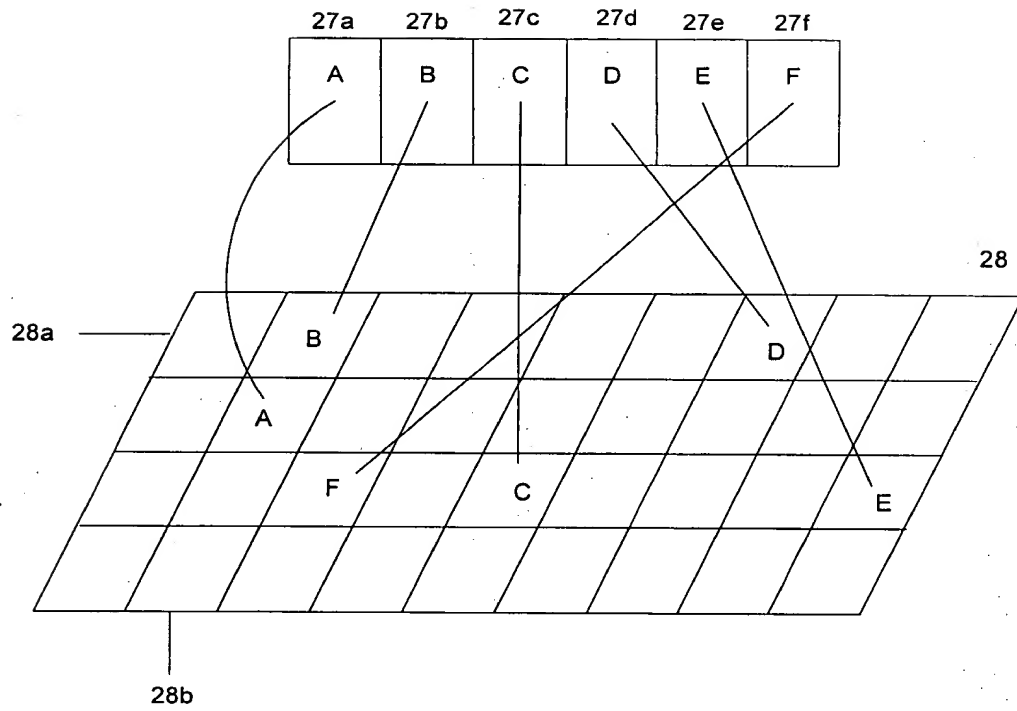


FIG. 2C

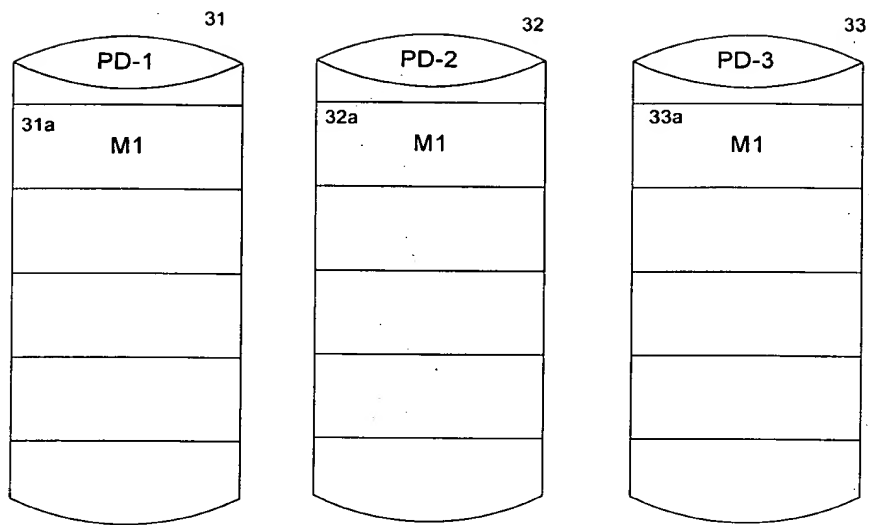


FIG. 3A PRIOR ART

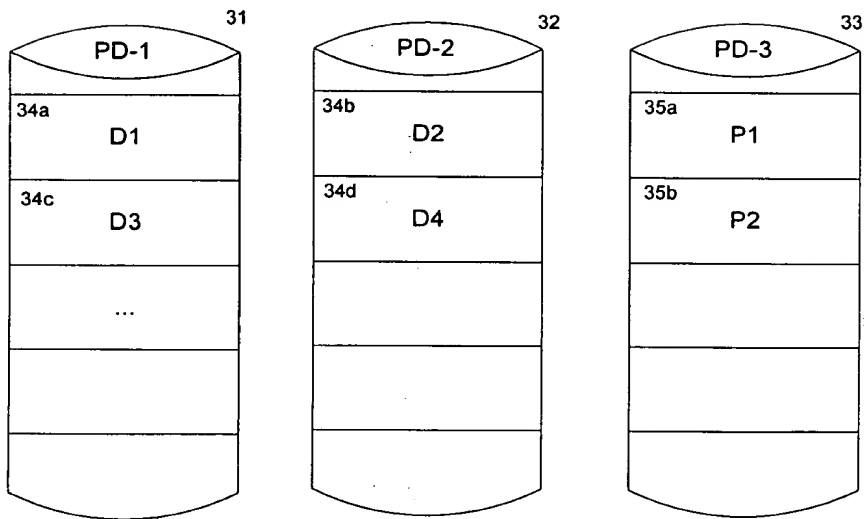


FIG. 3B PRIOR ART

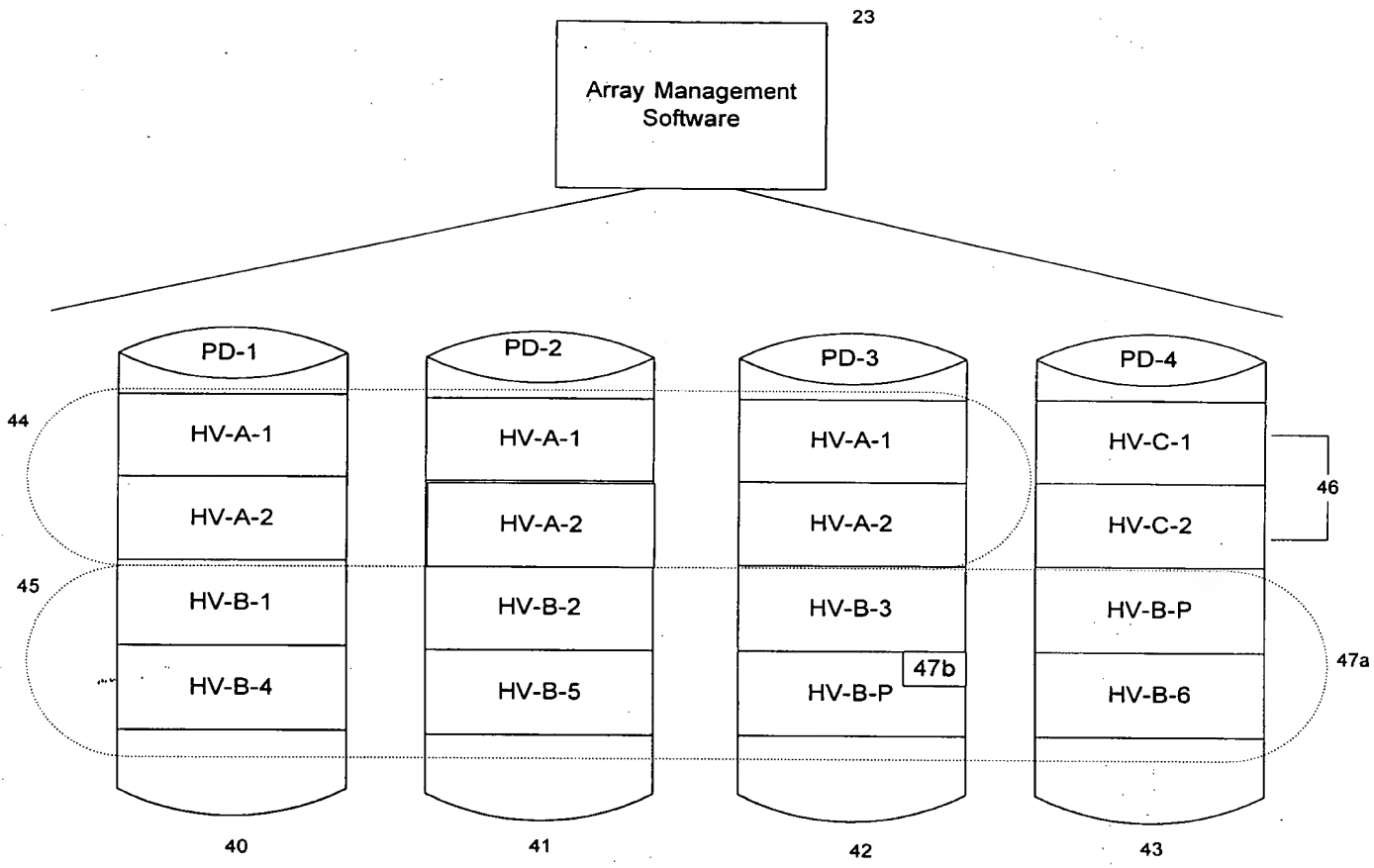


FIG. 4 (PRIOR ART)

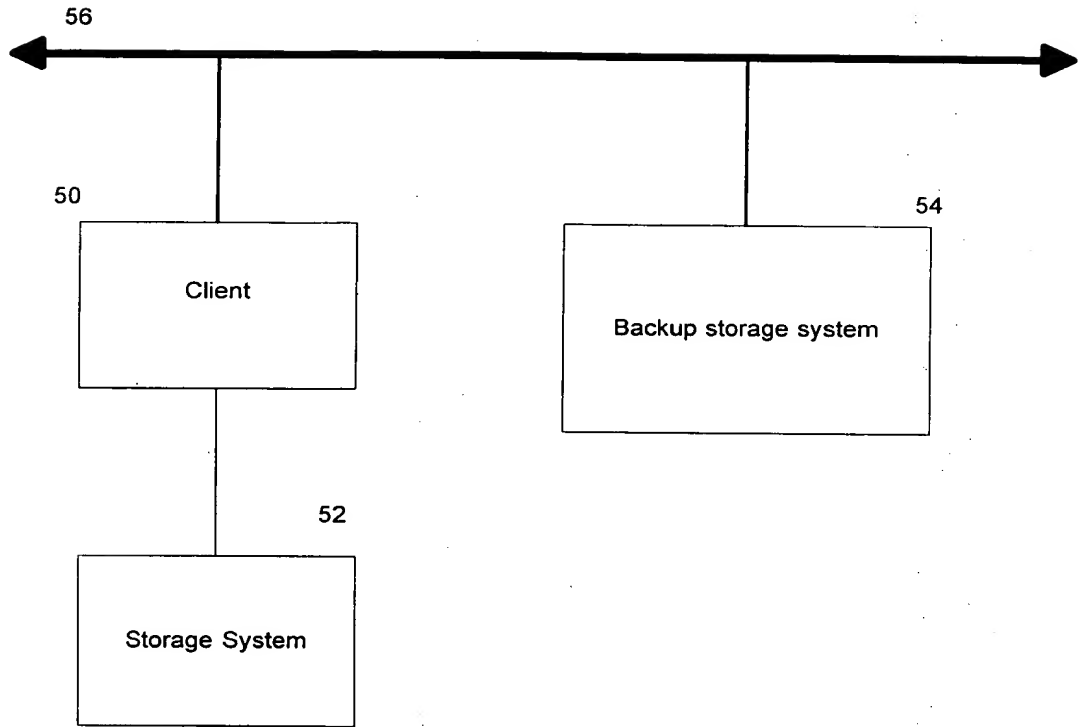


FIG. 5

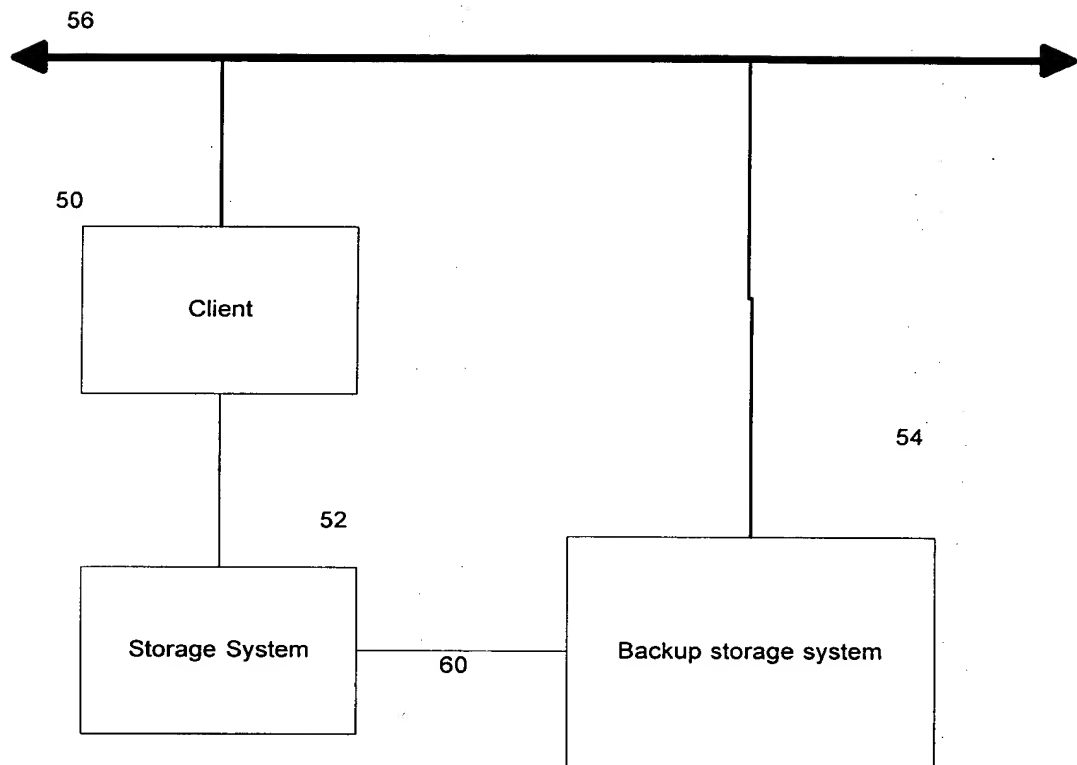


FIG. 6

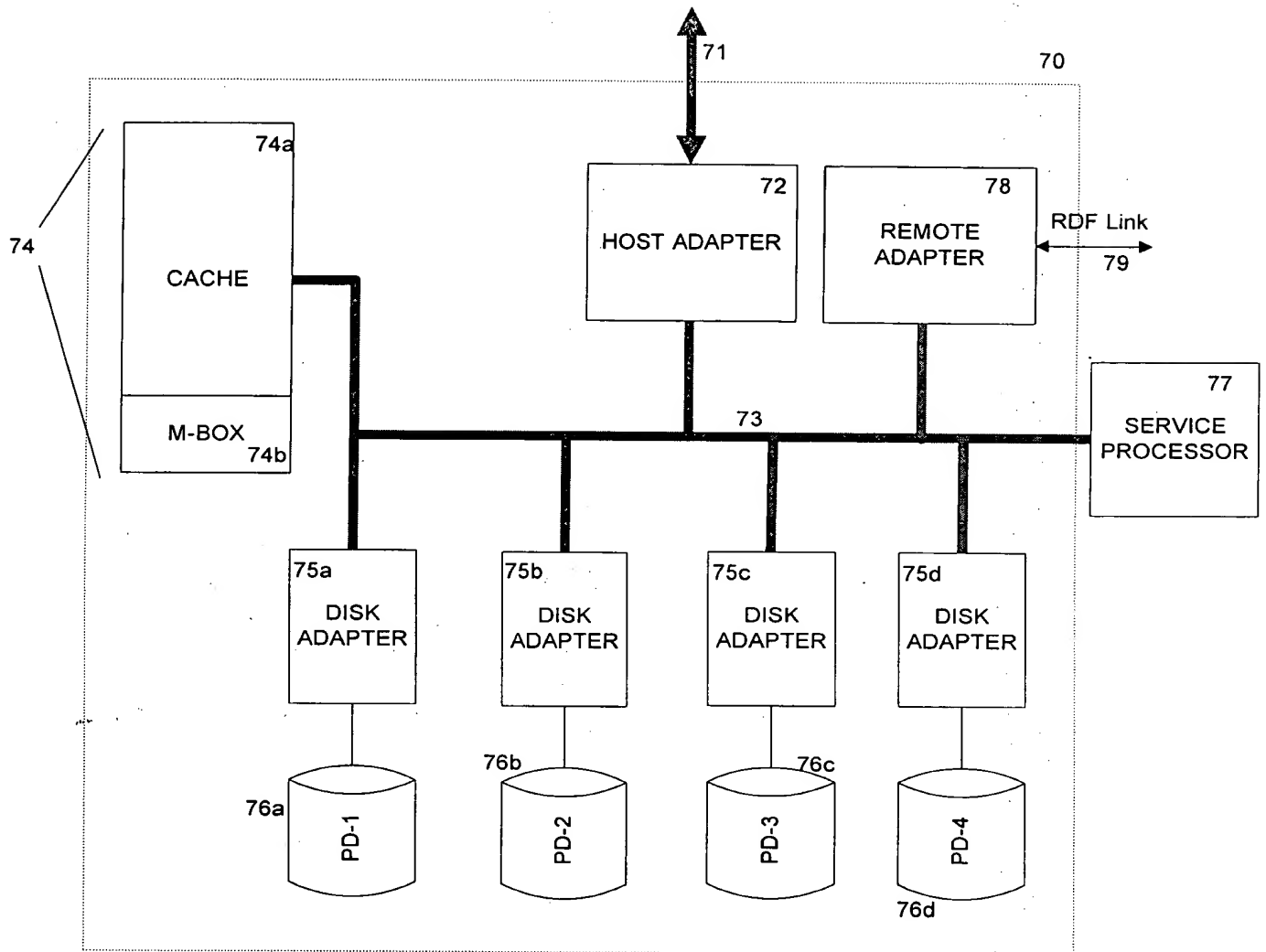


FIG. 7

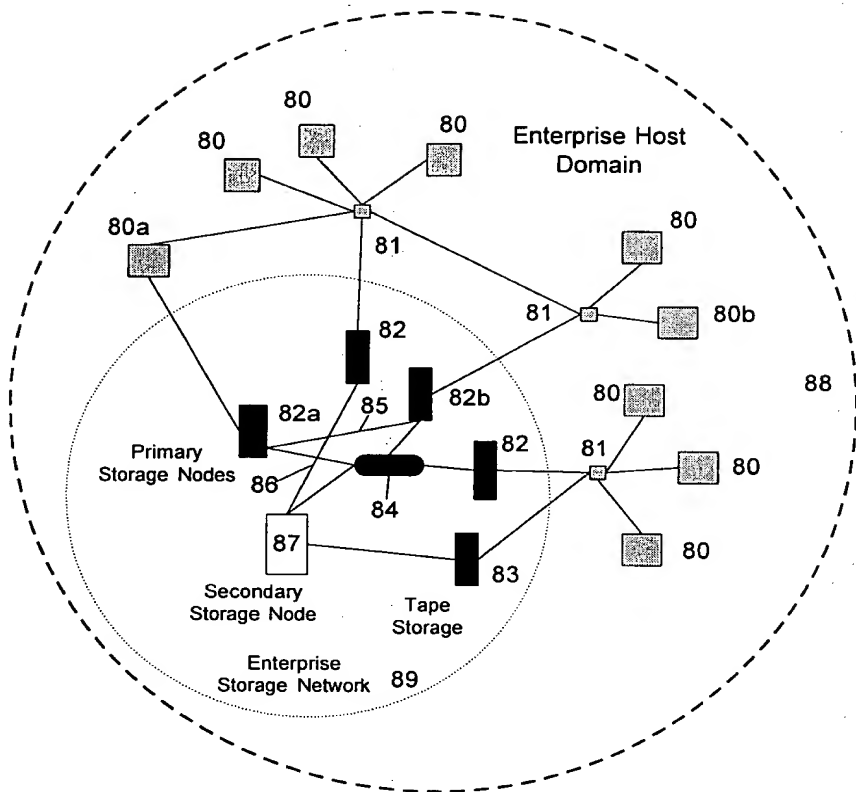


FIG. 8

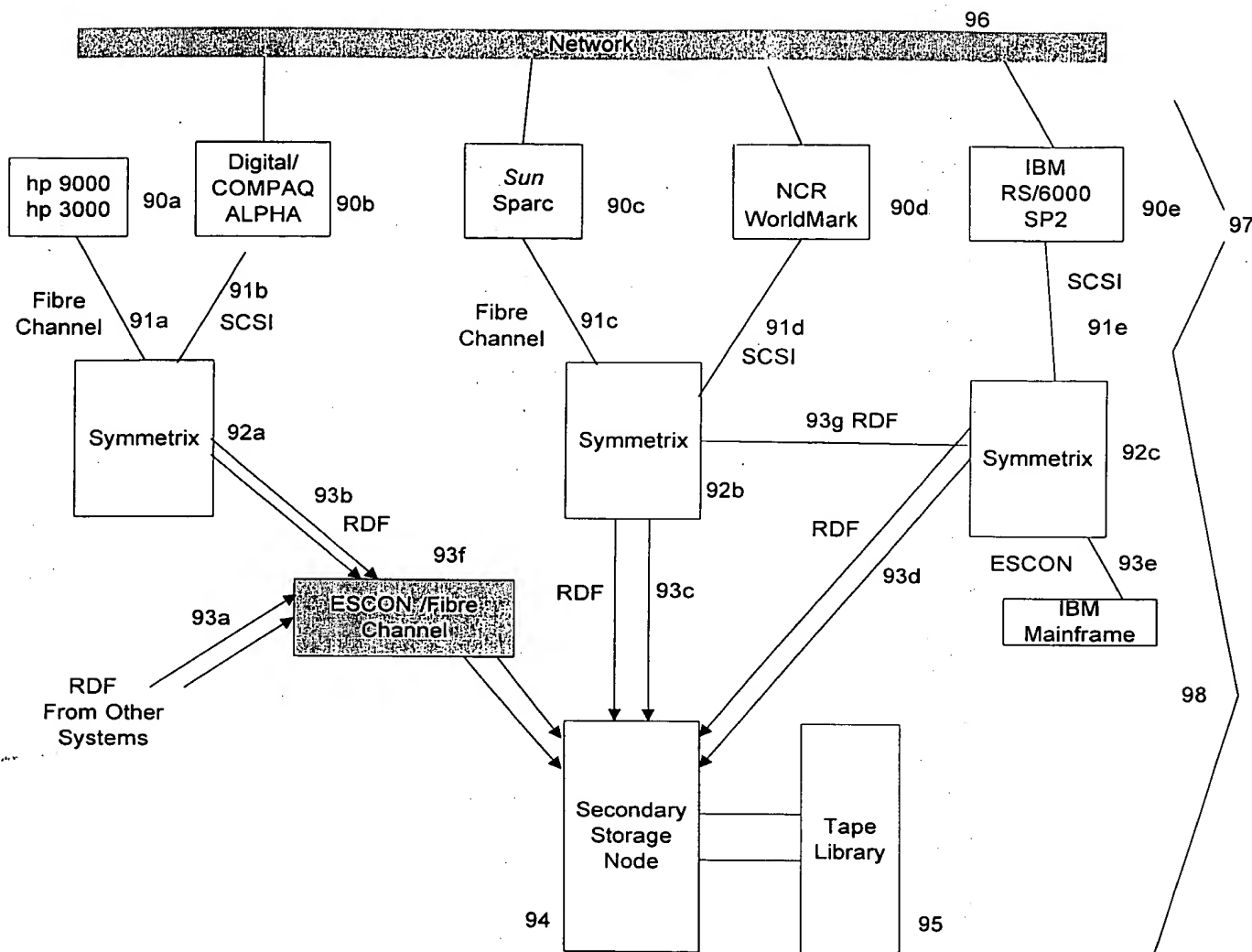


FIG. 9

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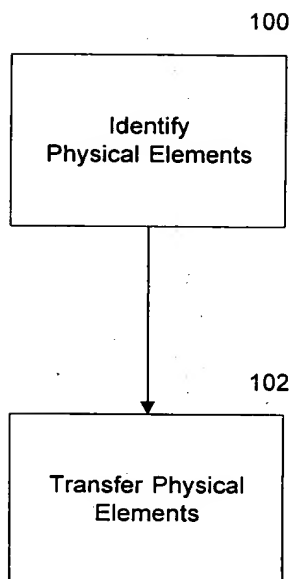


FIG. 10

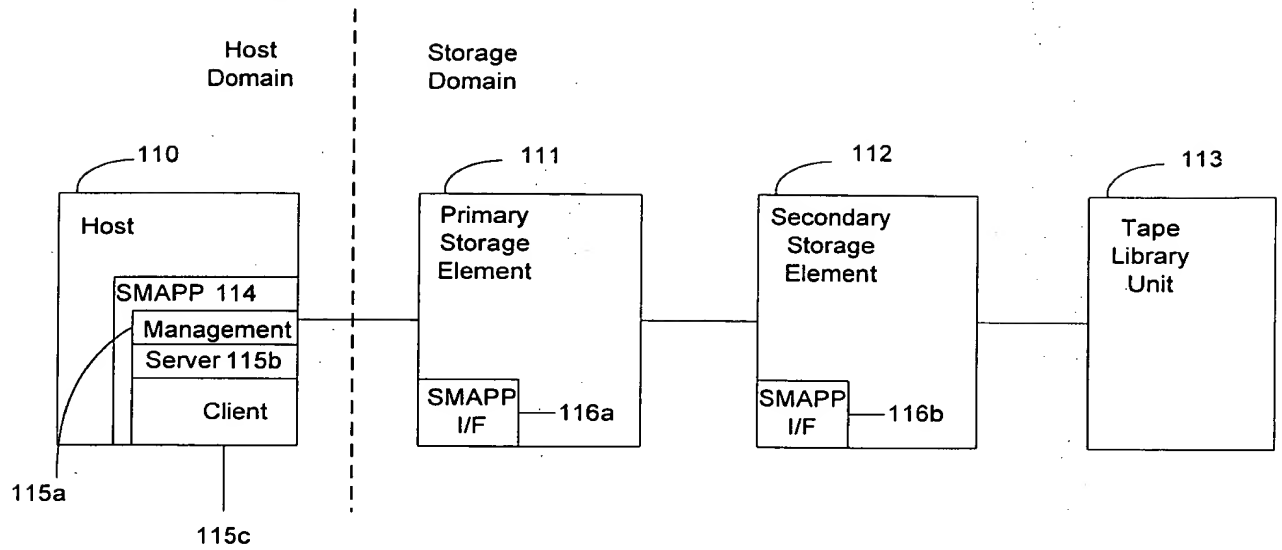


FIG. 11A

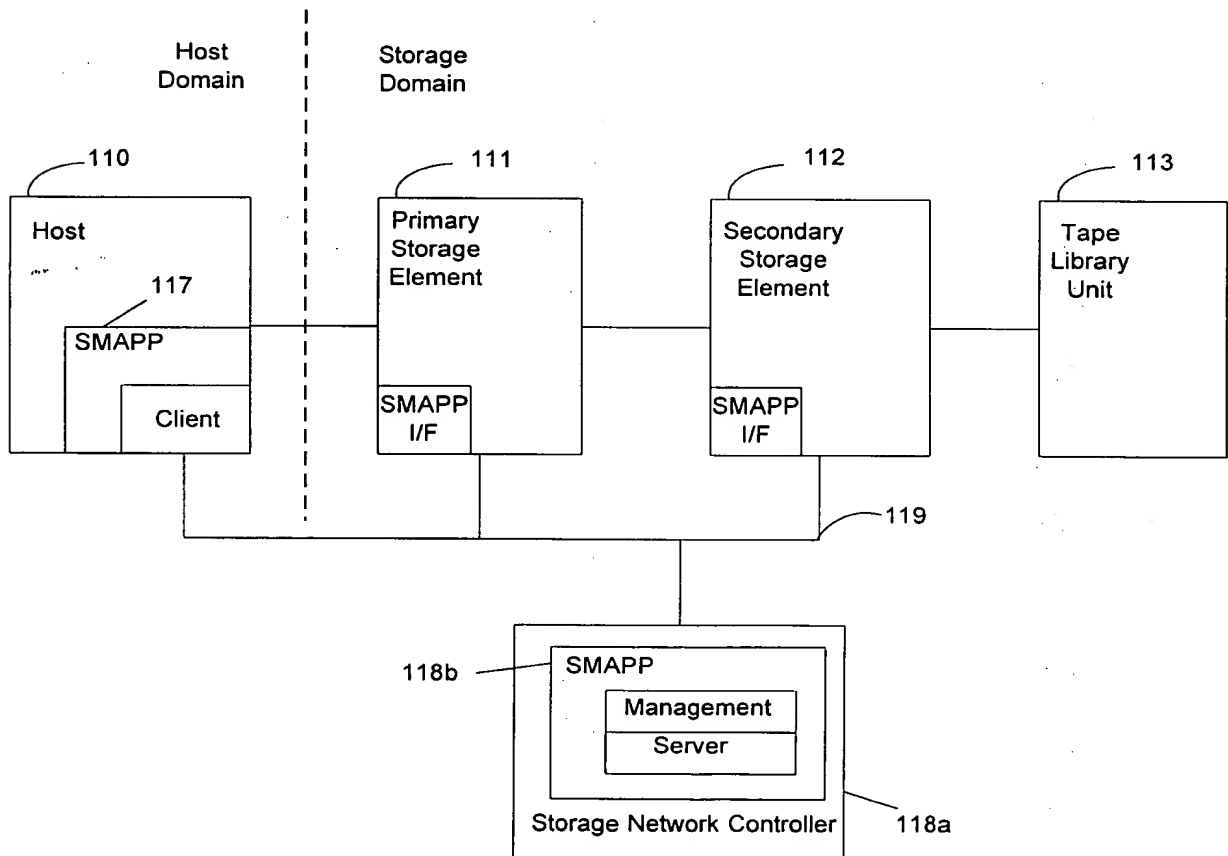


FIG. 11B

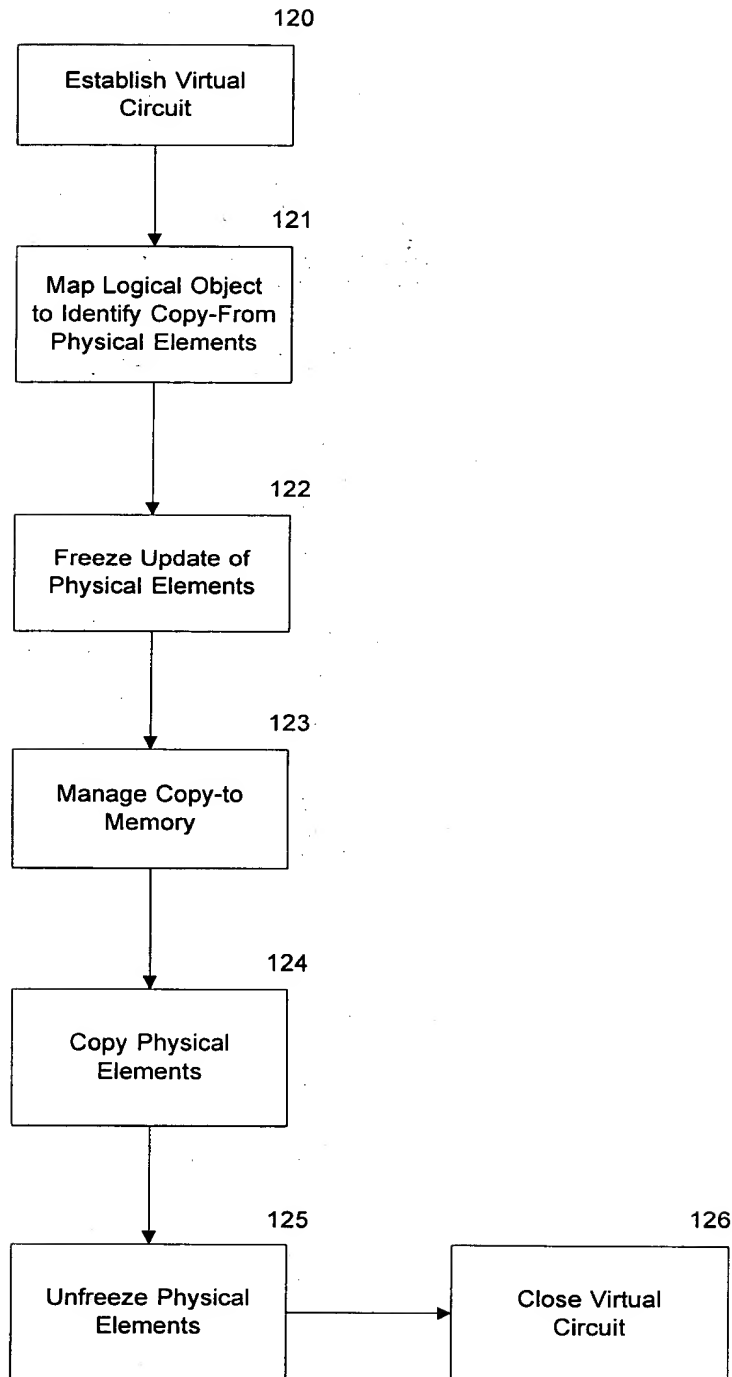


FIG. 12

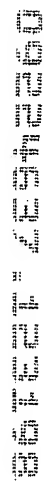


FIG. 13

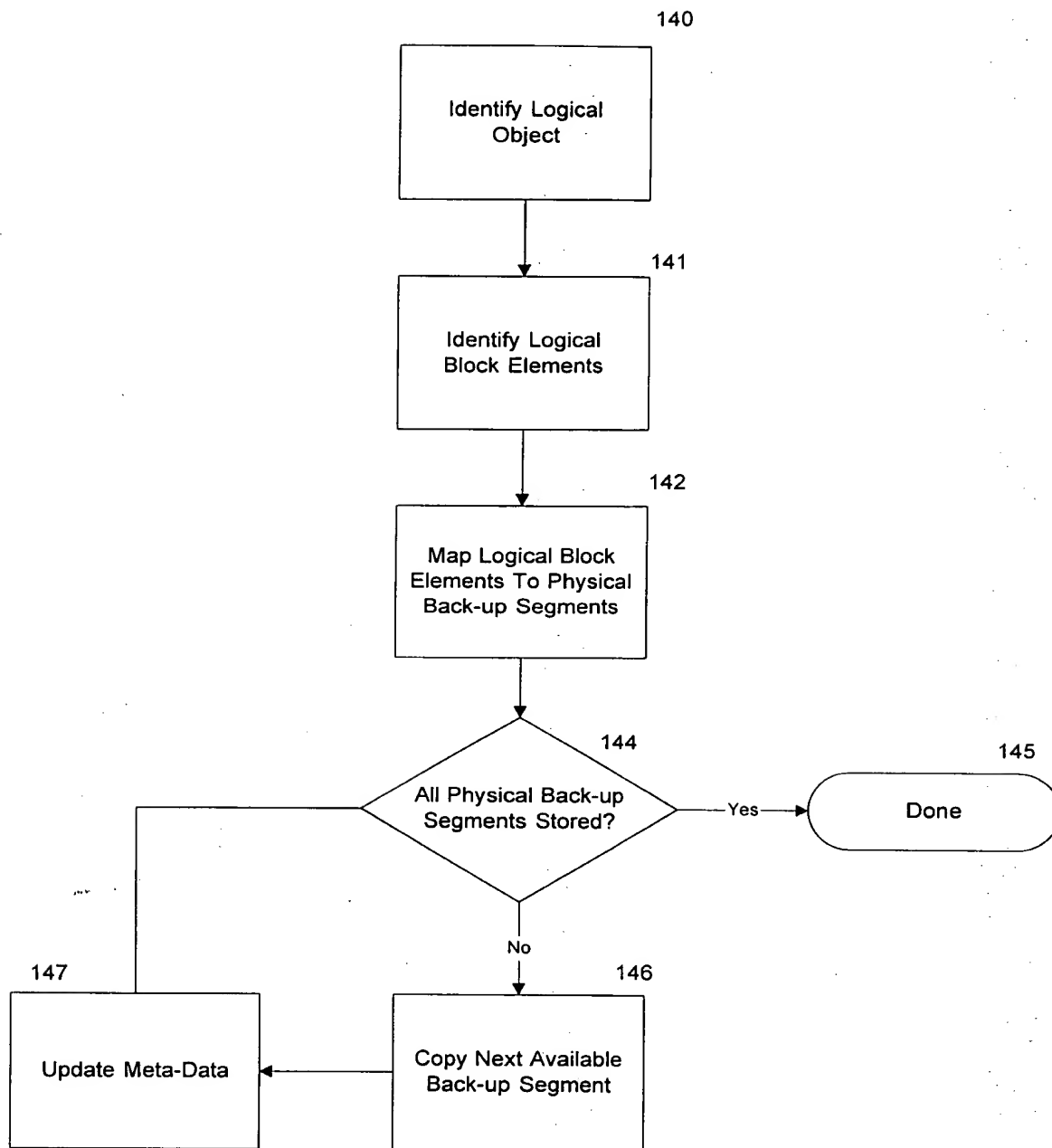


FIG. 14

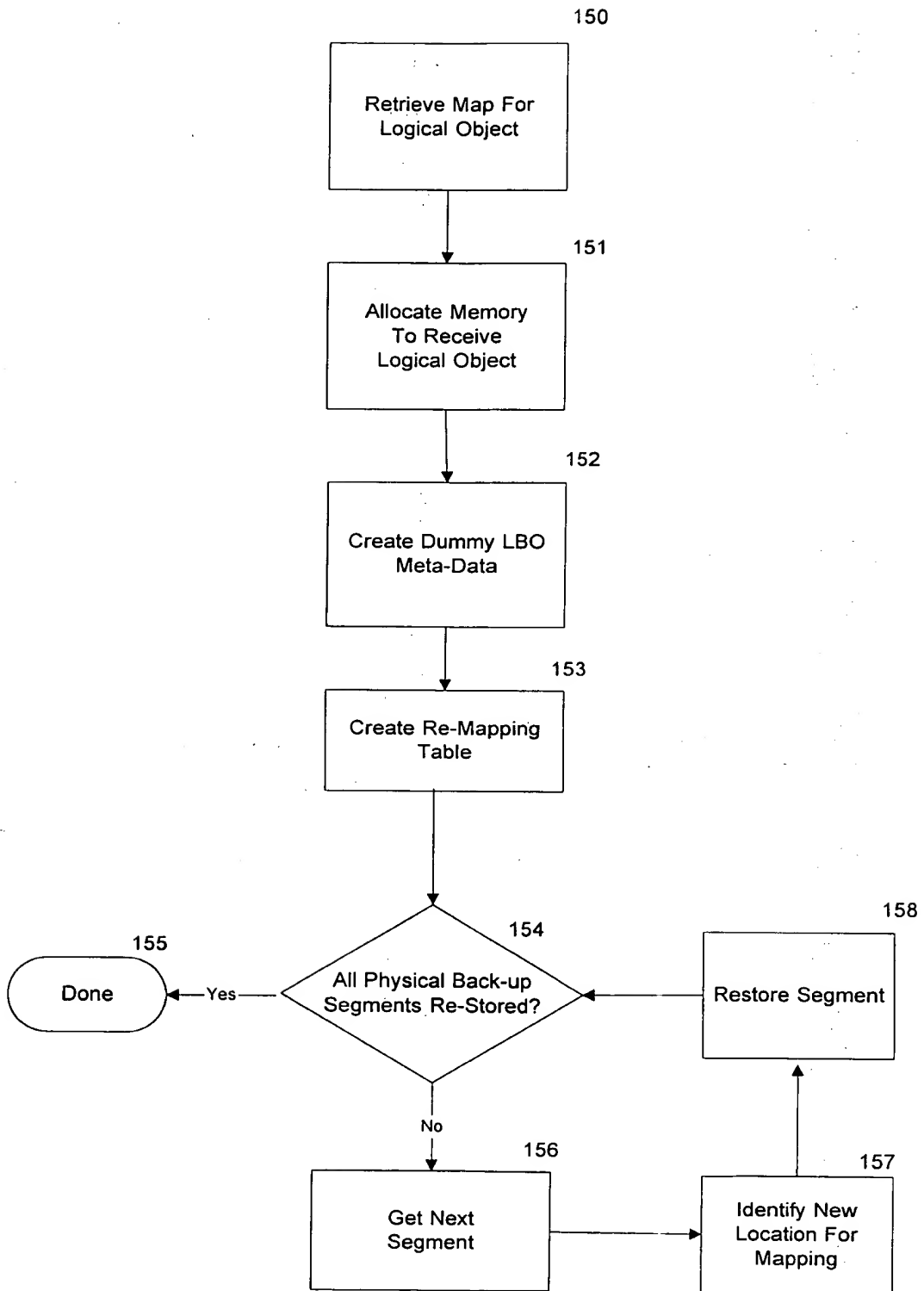


FIG. 15

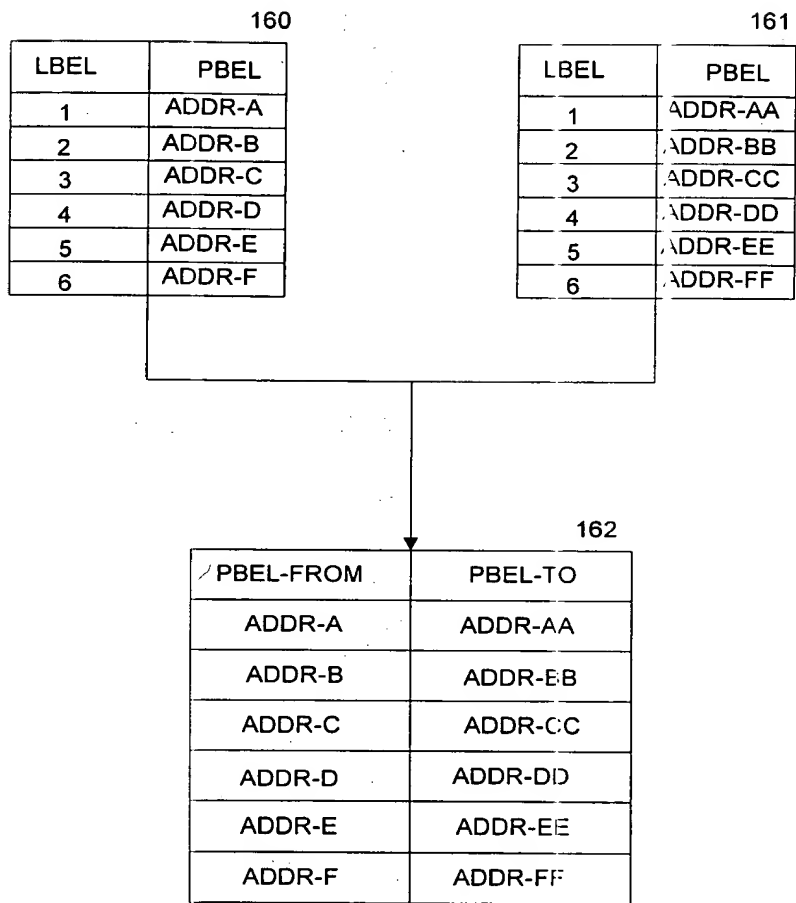


FIG. 16

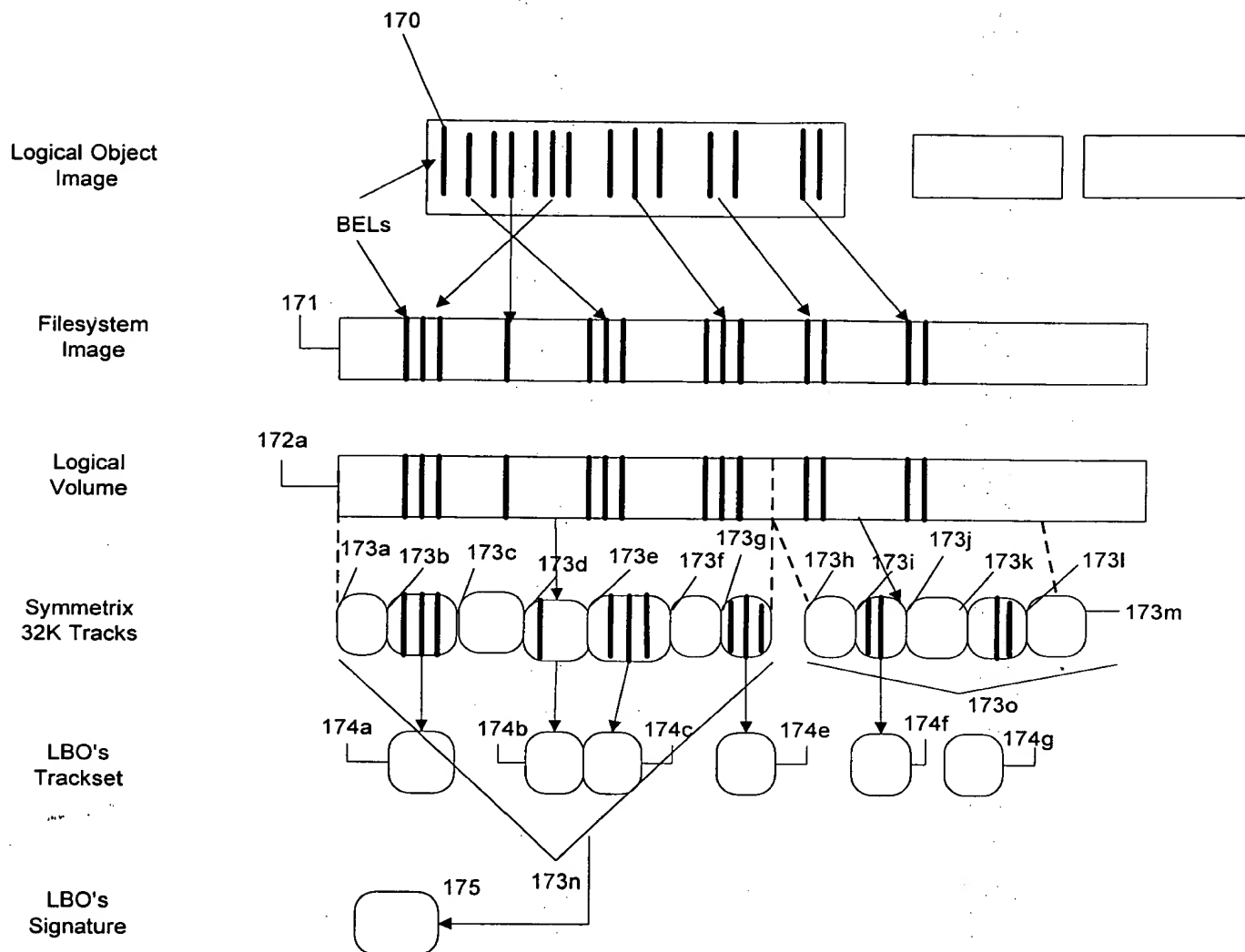


FIG. 17

LBEL	ADDR
1	DD:CCCC:H:OFFSET (0)
2	DD:CCCC:H:OFFSET (1)
3	DD:CCCC:H:OFFSET (0)
4	DD:CCCC:H:OFFSET (1)
5	DD:CCCC:H:OFFSET (2)

FIG. 18

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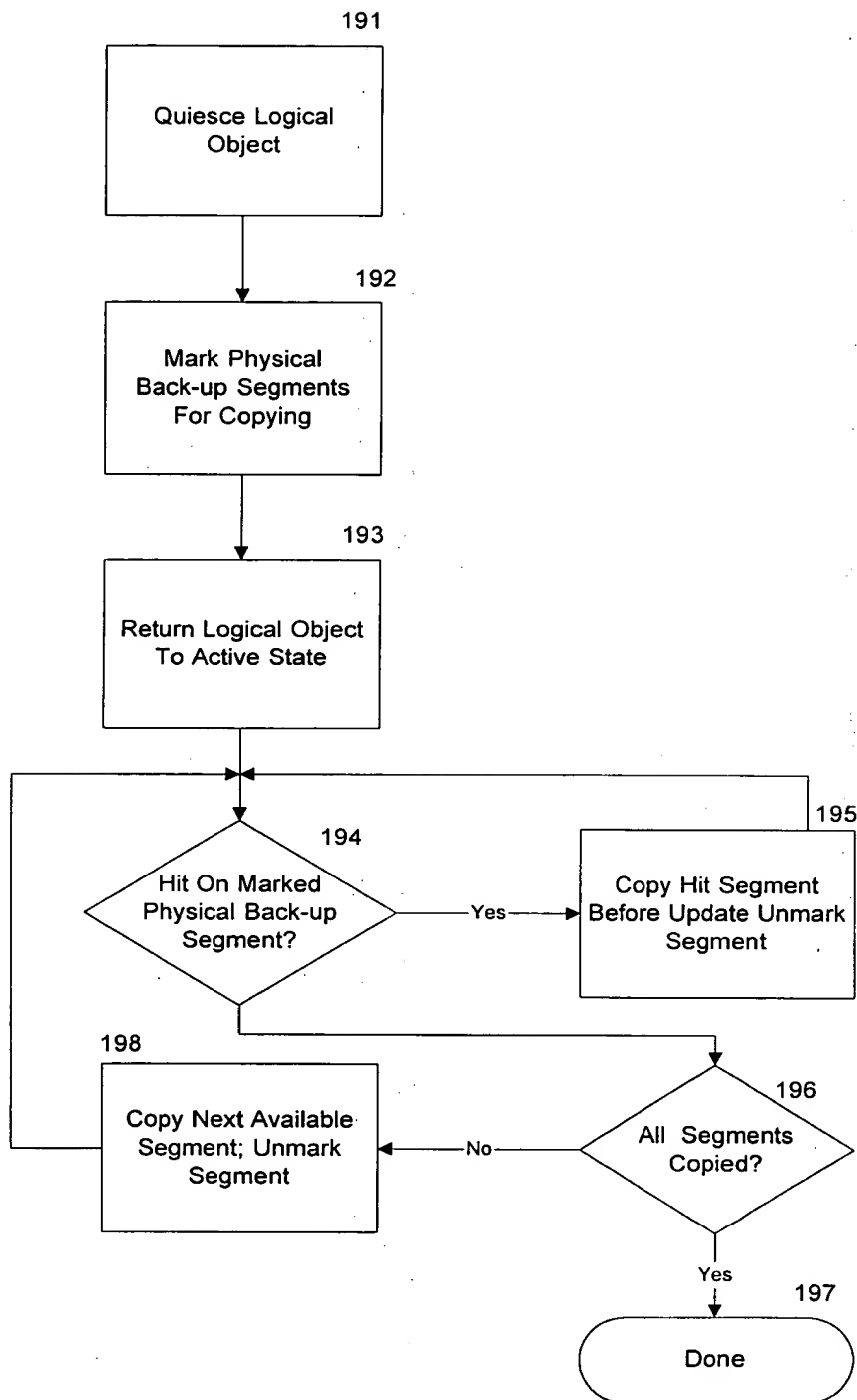


FIG. 19

604247 2 2442350

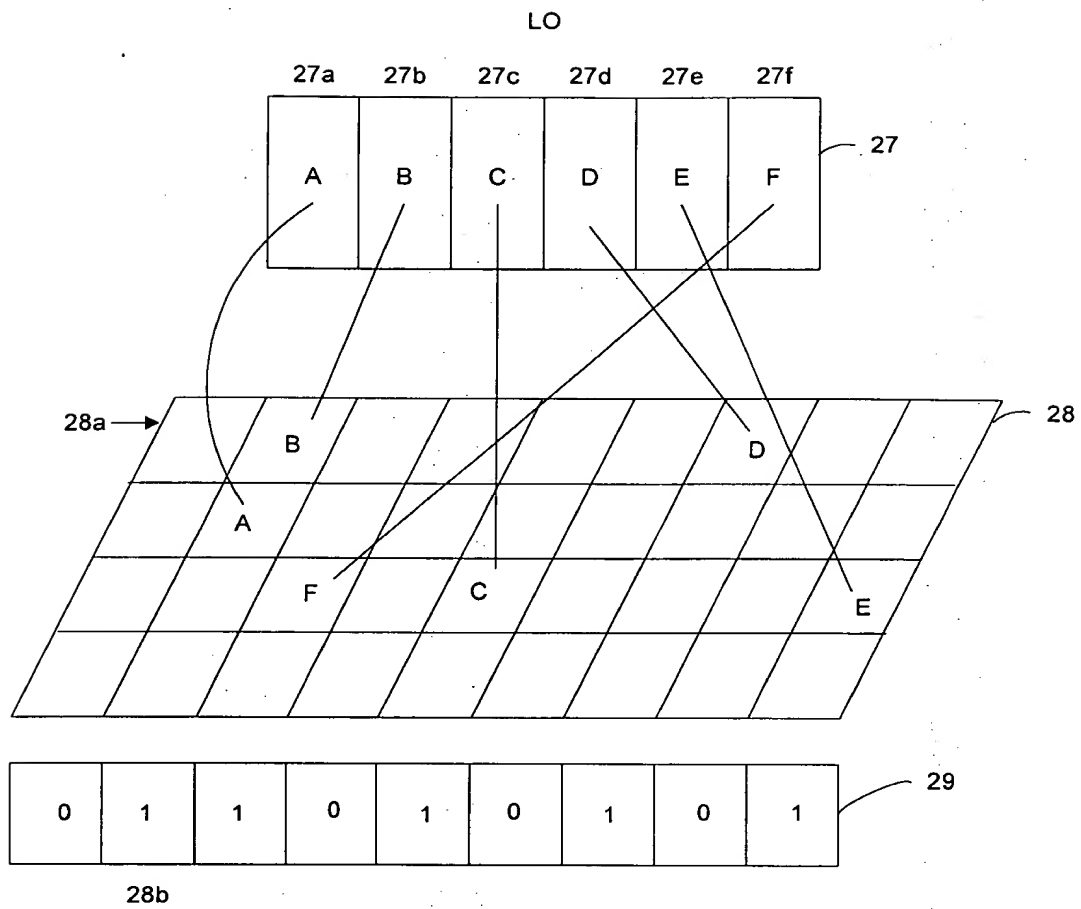


FIG. 20

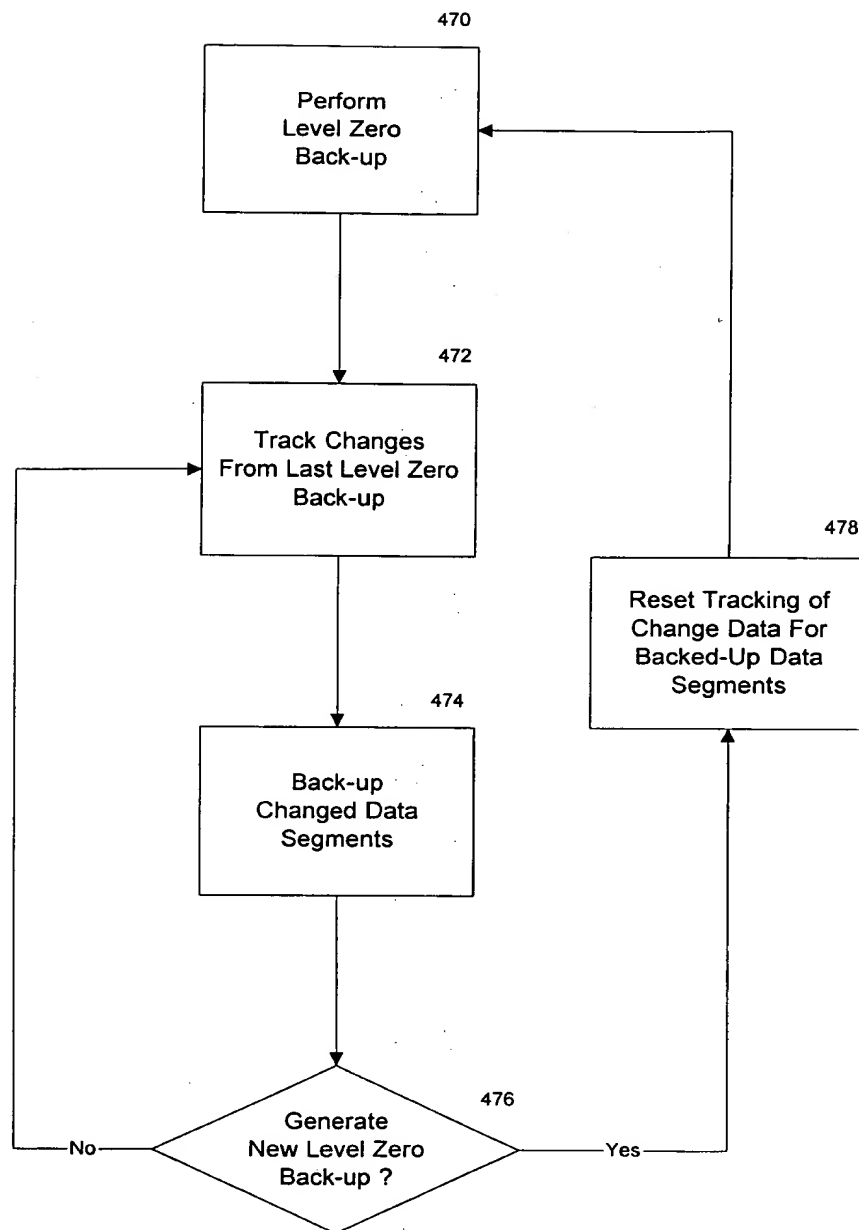


FIG. 21

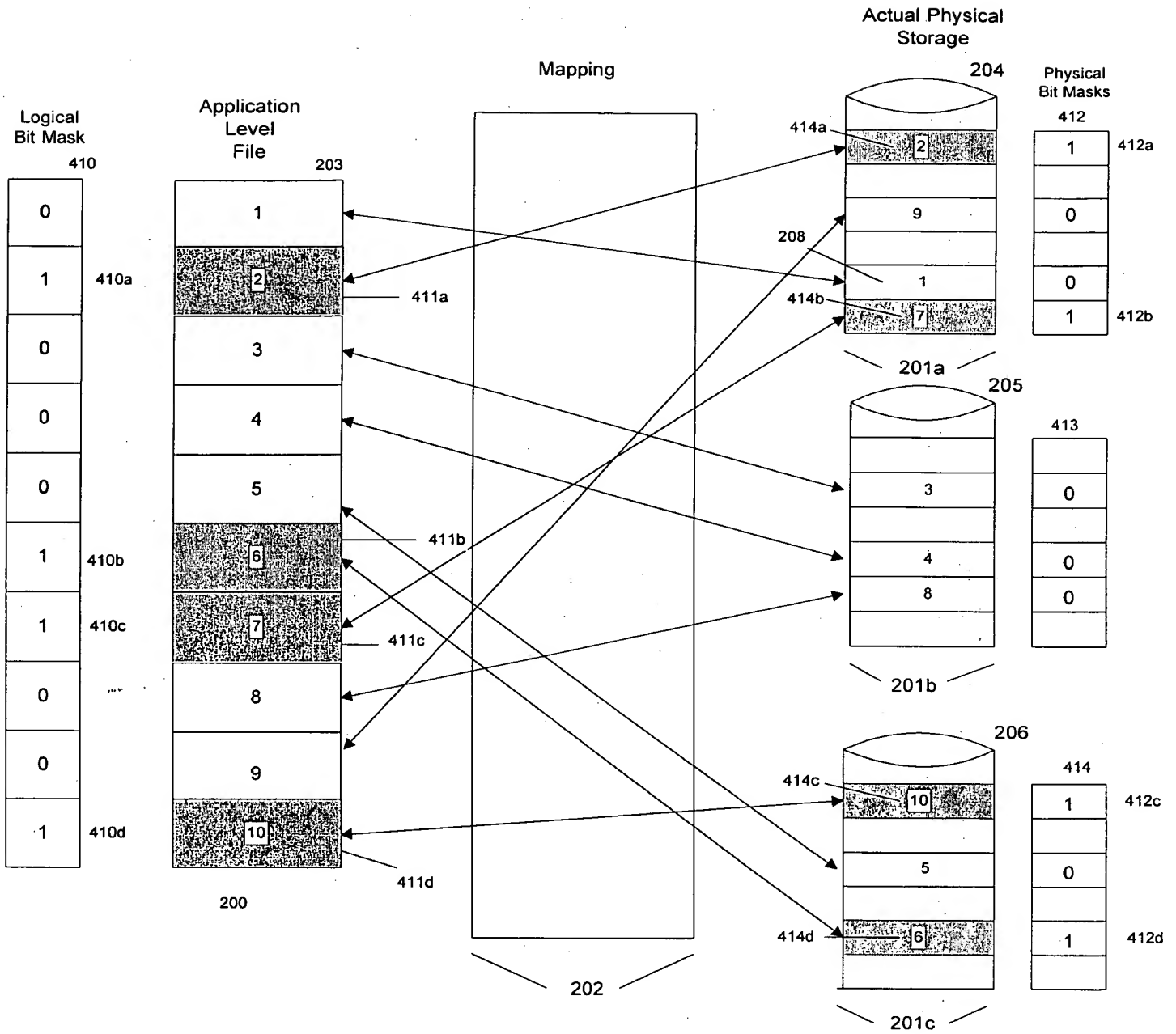


FIG. 22

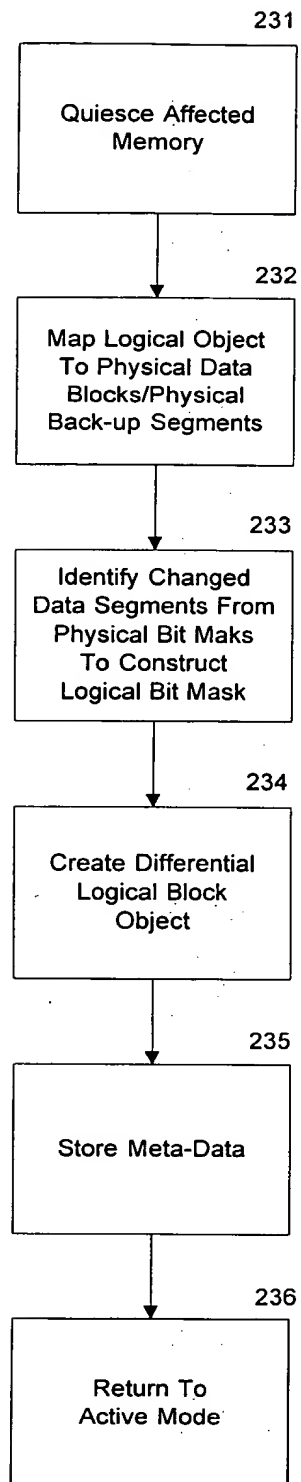


FIG. 23

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B6E6F7 2E942260

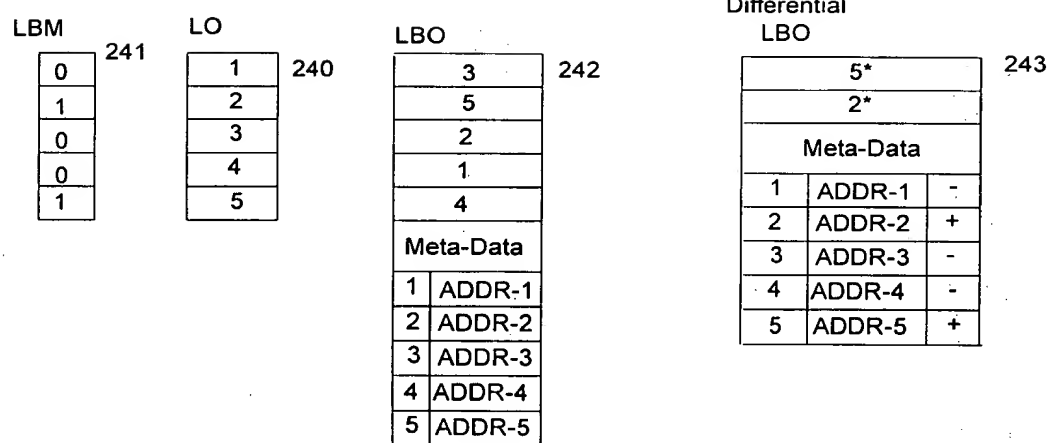


FIG. 24

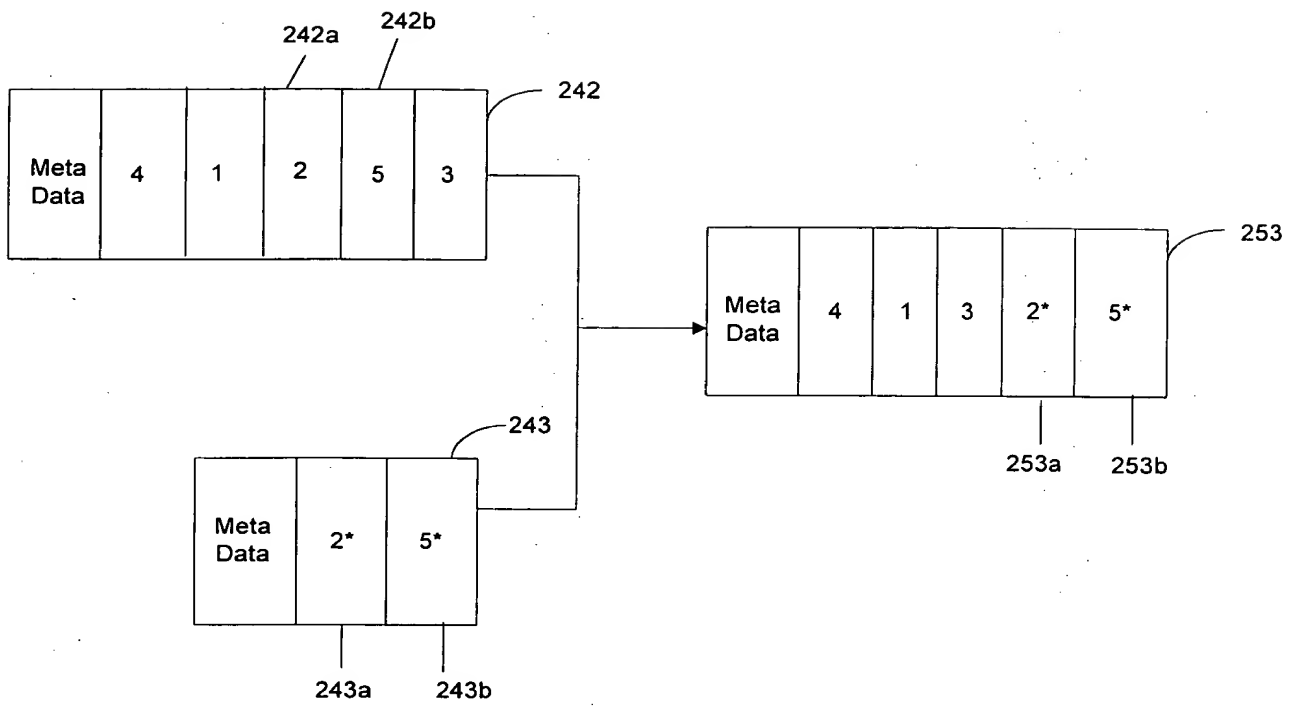


FIG. 25

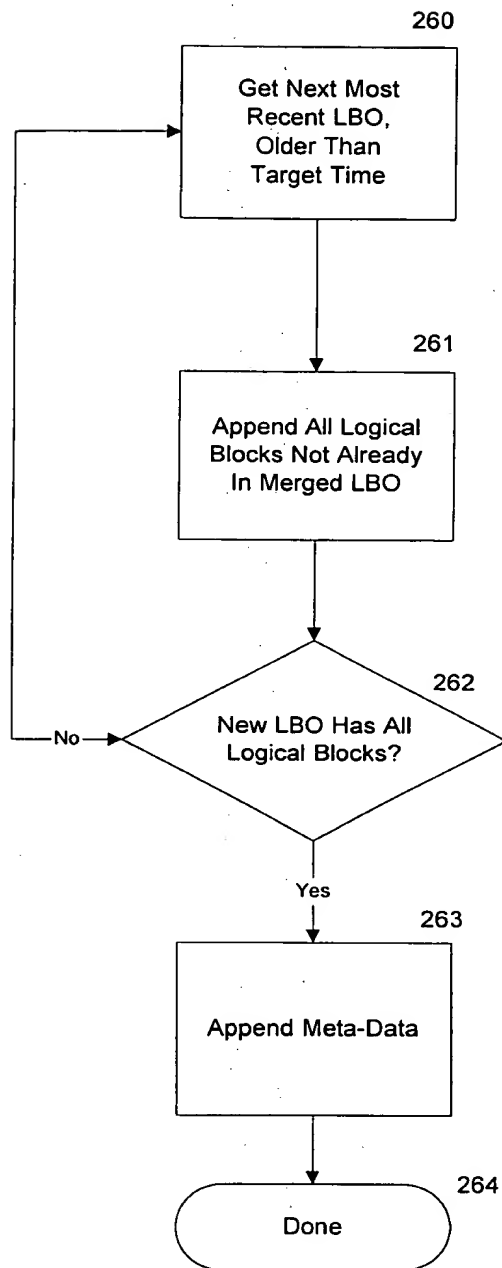


FIG. 26

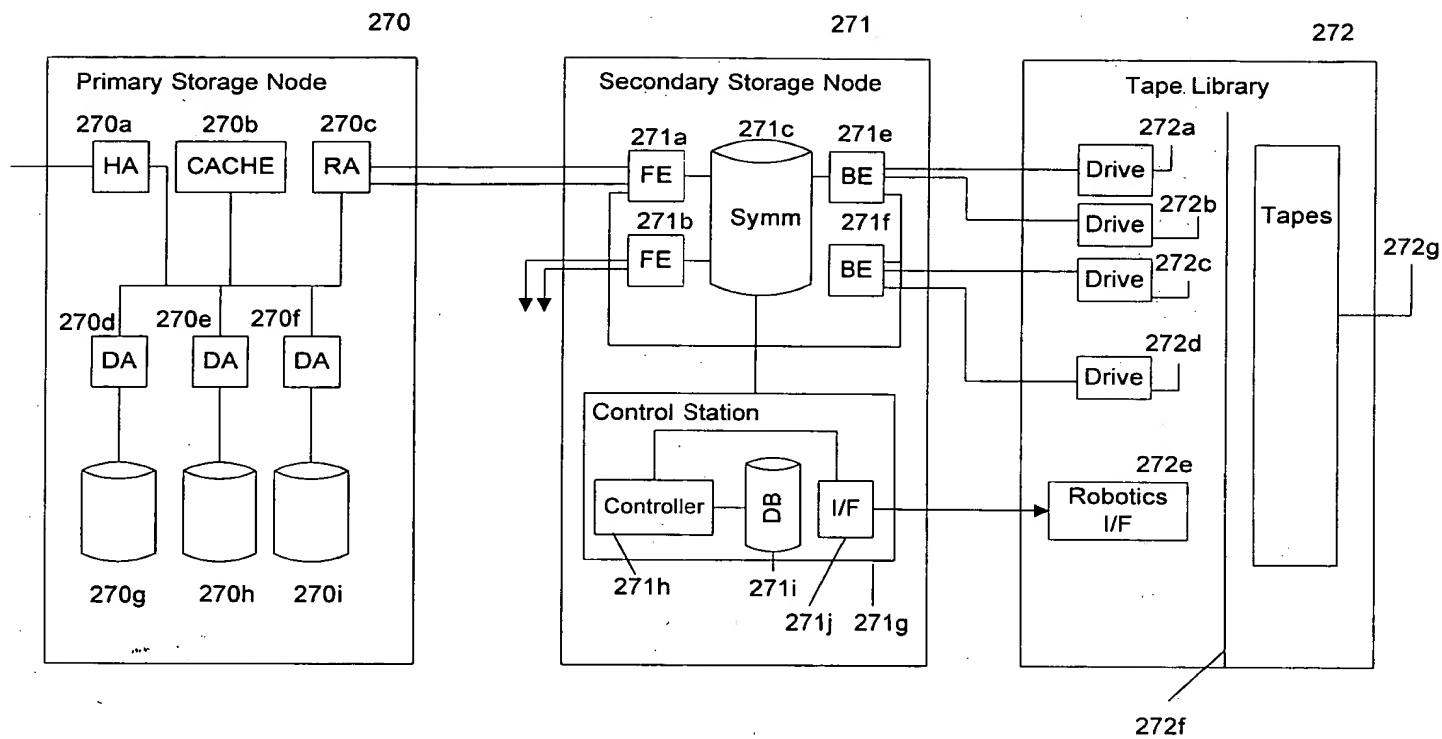


FIG. 27

VC_MREQ VC_BOT VC_WRITE VC_EOT ERROR

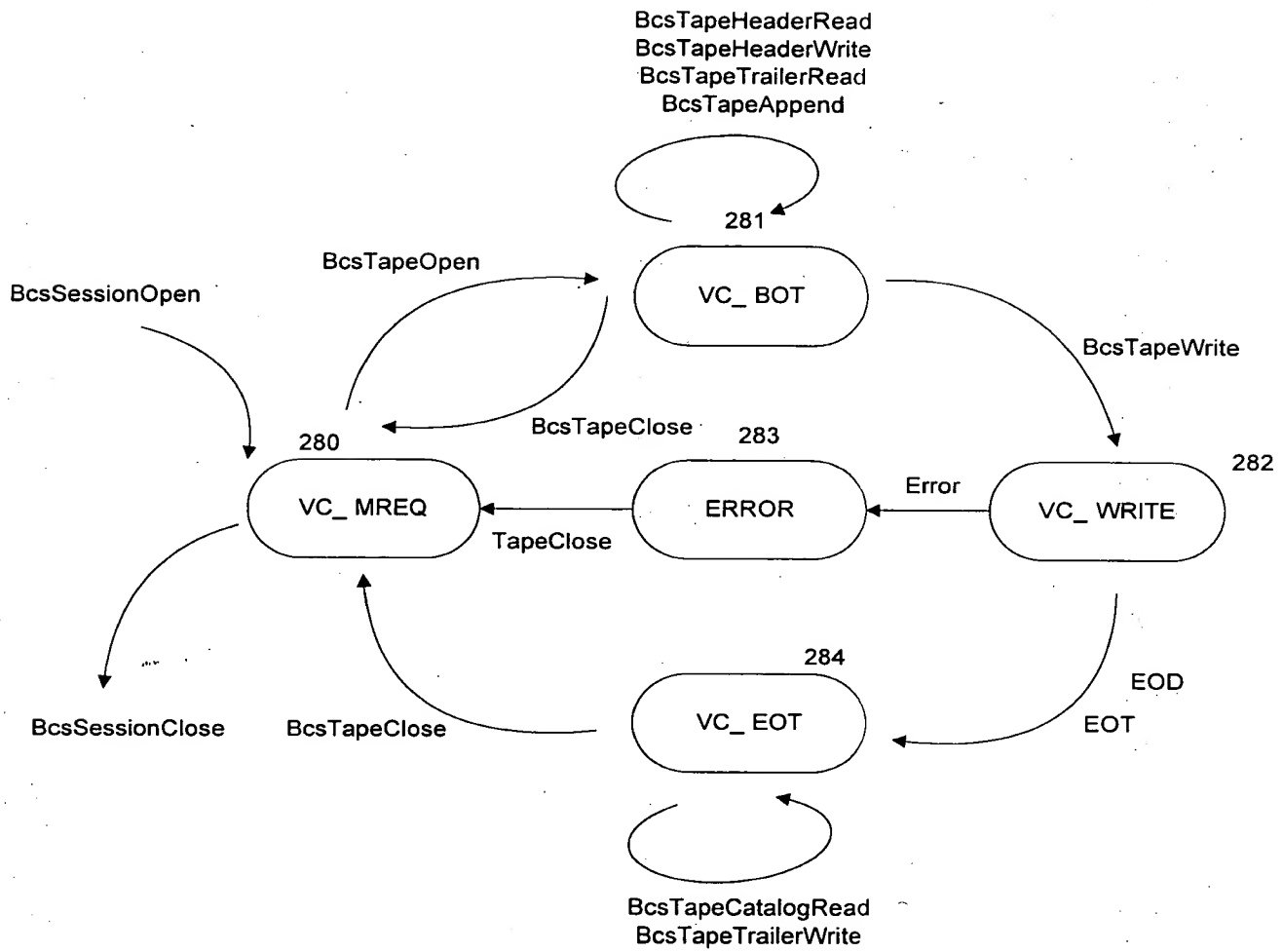


FIG. 28

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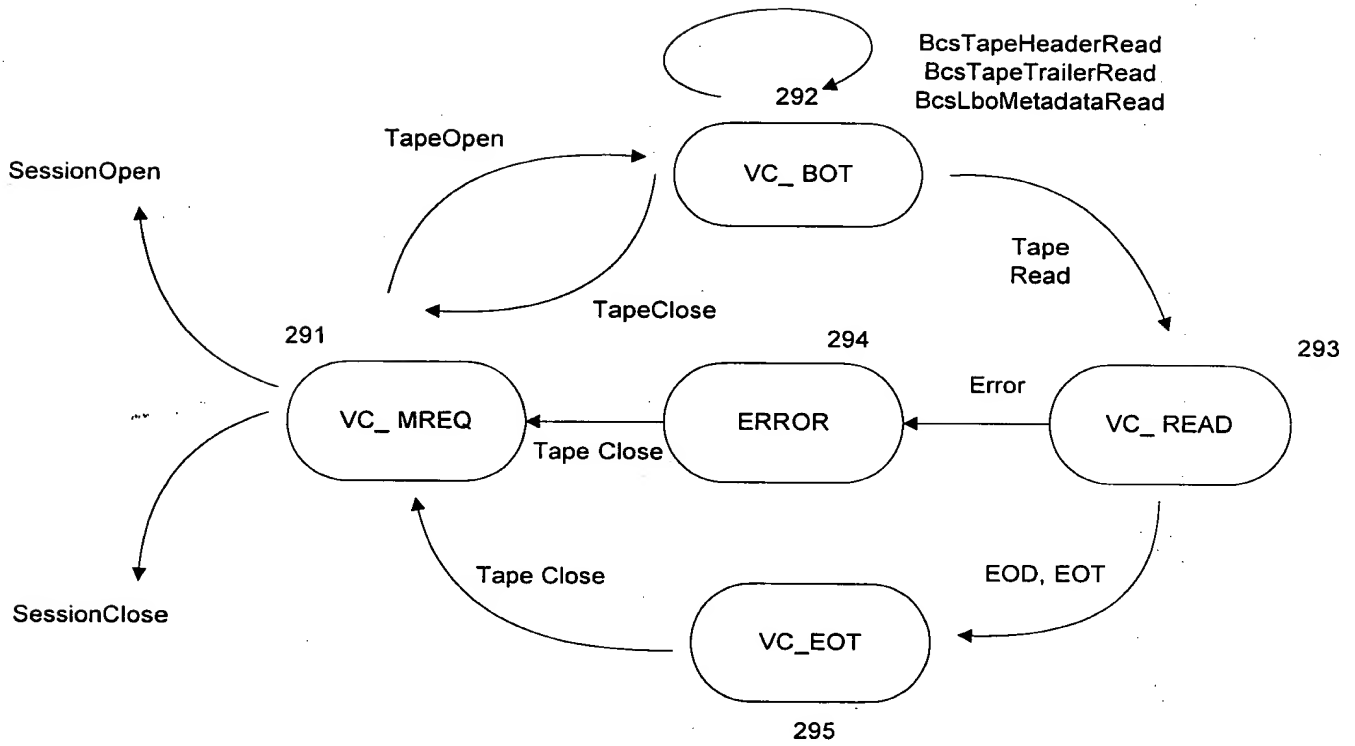


FIG. 29

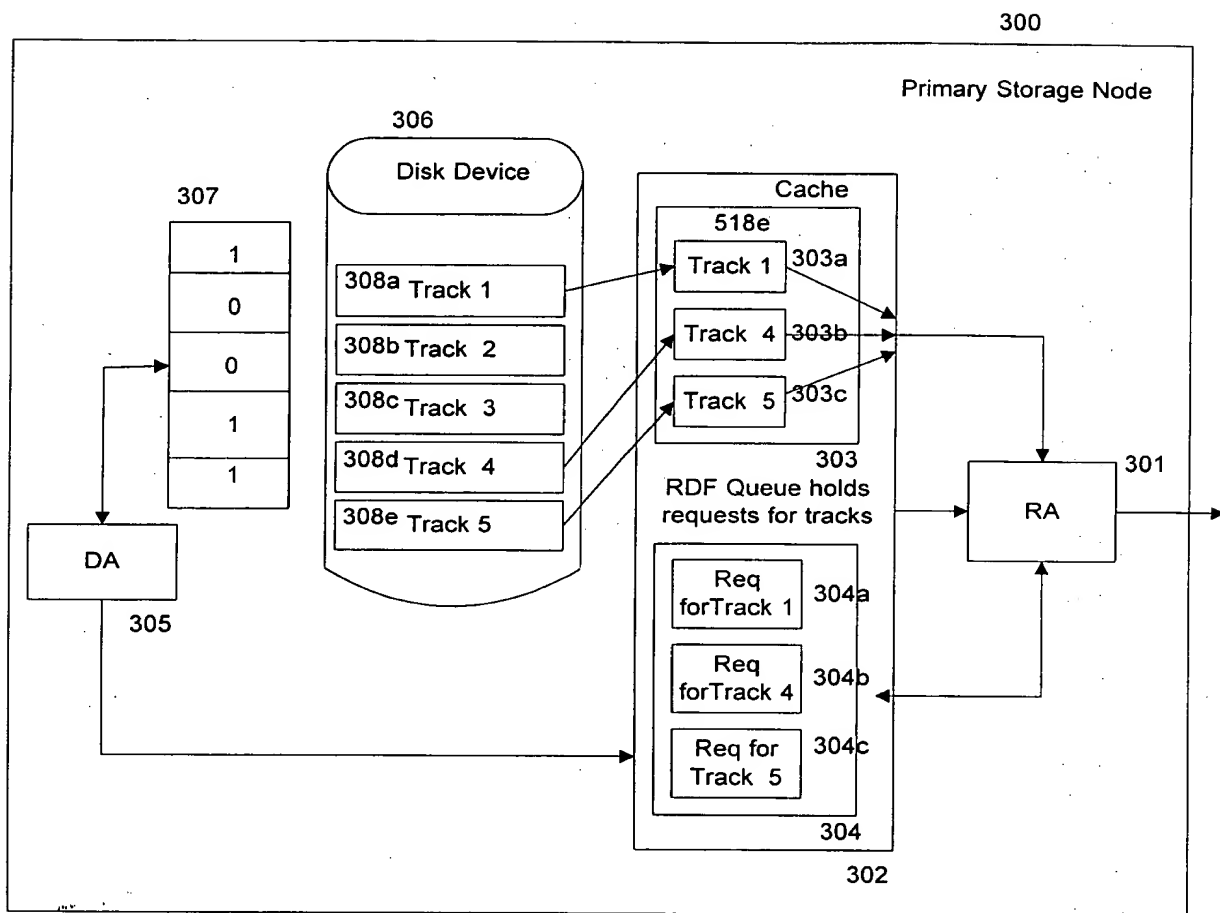


FIG. 30

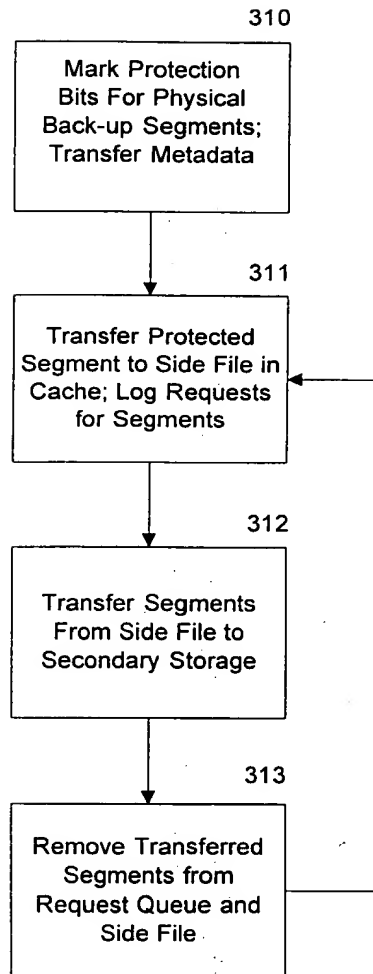


FIG. 31

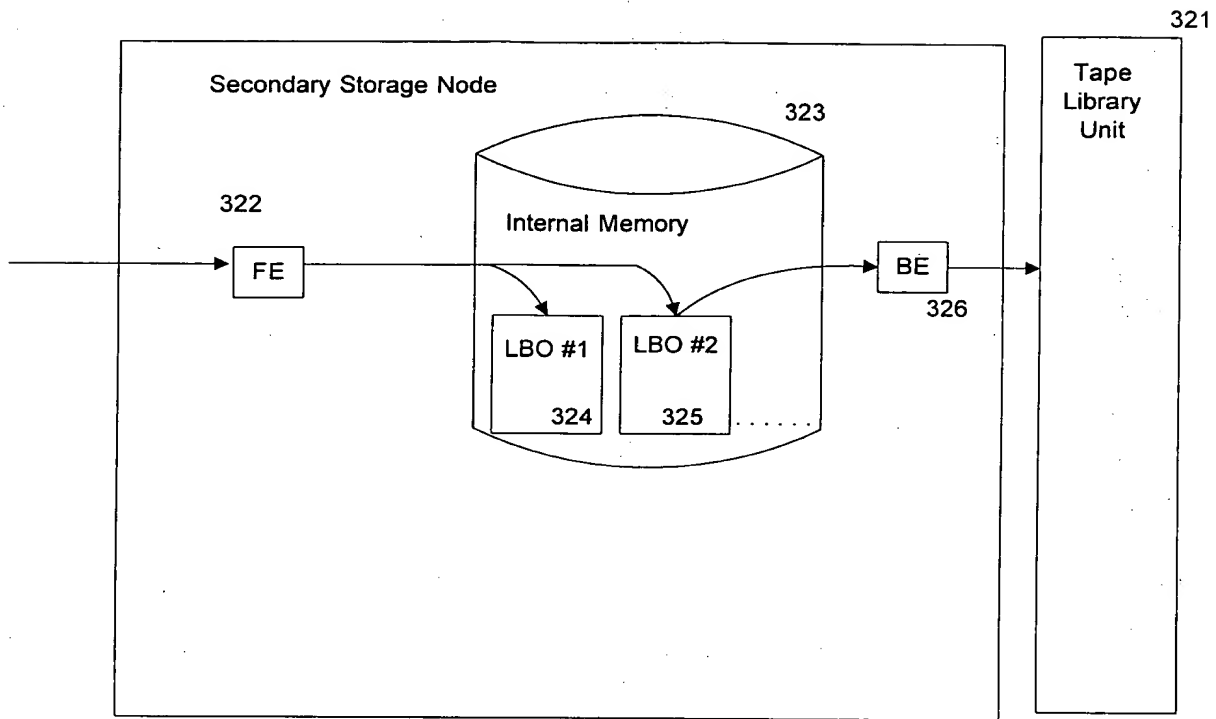


FIG. 32

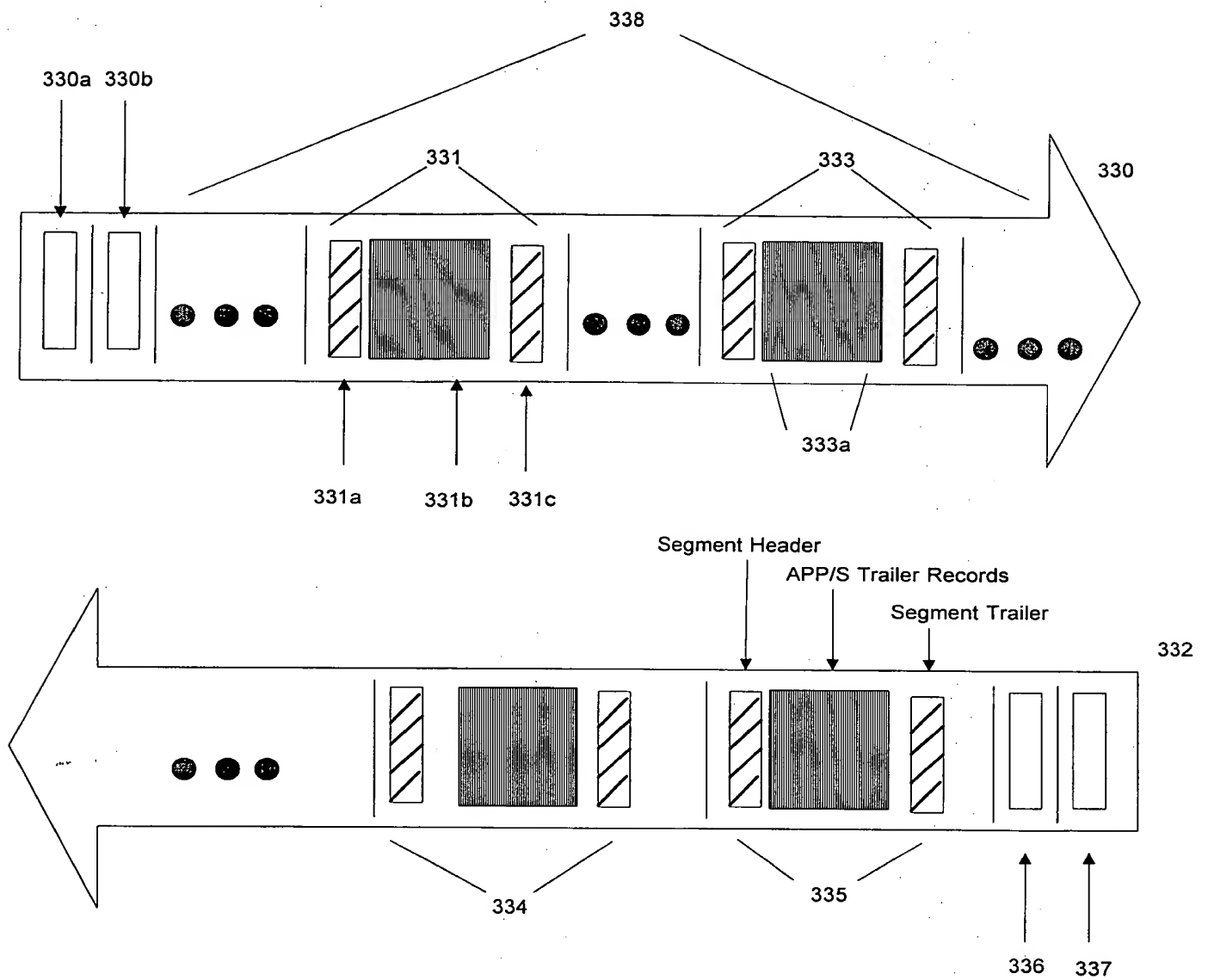


FIG. 33